Memory Expansion Banks 1–3

0 0 0 0 | 0 0 0 0 | 0000

Address

Valid Access

Off / On

Invalid

If green, at least one Bank of the Memory Expansion is enabled
Use push buttons to enable Banks

Valid–LEDs here indicate whether data is invalid (red), a operation has been com–pleted (green) or PEC is trying to access a not enabled bank (orange). Addresses already valid than

0 0 0 0 | 0 0 0 0 | 0000

Data

Valid Acc.

Write / Read

Shows whether last access was read or write operation

Inval

Disable ALL | Enable Bank1 | Enable Bank2 | Enable Bank3

Reset | Stop | Merge PPT & TTY in

DMA | Cont | Merge PPT & TTYout

IRQ | | In.In.

Update All | Update S | Update A | Update E | Update D | Trapu Active

Register S

0 0 0 0 | 0 0 0 0 | 0000

Valid

Pwr fail OK

NoUpd

Monitors the Reset Signal from the power supply

The Valid–LEDs in this block indi–cate, that the lines are updated and that valid data is shown. Use push–buttons to select what is being updated

Register A

0 0 0 0 | 0 0 0 0 | 0000

Valid

IRQ level Std

Indicates PEC running at interrupt level

NoUpd

Register E

0 0 0 0 | 0 0 0 0 | 0000

Valid

DMA Off

DMA line asserted by Transputer for accessing PEC's core memory

NoUpd

Register D

0 0 0 0 | 0 0 0 0 | 0000

Valid

Reset Run

NReset–Signal distributed throughout PEC

NoUpd

Vcore

13.00

Vsense

−5.00

Vprocessor

5.00

Vtrapu

5.00

0 0 0 0 | 0 0 0 0 | 0000

LED indicates if data has been read since last reset of PEC

Read

Keys Trapu

Indicates if data displayed and sent to PEC is the Trapu's register or data from the hand keys

NoAcc

Word Generator

WDG Source | Dec Octal | Test Mode